

DEPARTMENT OF ELECTRONICS ENGINEERING

SCHOOL OF ENGINEERING AND TECHNOLOGY

PONDICHERRY UNIVERSITY (A Central University)

Kalapet, Puducherry -14.

20.01.2025

Ph.D (Electronics and Communication Engineering): Admission 2024 - 2025 Instructions to the Candidates shortlisted for the Interview

- 1. The interview for the short listed Ph.D candidates will be conducted on 05th February (Wednesday) through offline mode.
- 2. The Ph.D candidates who are called for interview should submit the following documents for verification on the date of interview.
 - X Std or equivalent certificate/mark list along with age proof
 - H.Sc. or its equivalent certificate/mark statement
 - Degree/Provisional certificates of UG & PG Degree along with mark statements.
 - OBC with Non Creamy Layer certificate /EWS certificate recently issued (i.e. issued after 1st September, 2024) by Competent Authority (in case of OBC/EWs Candidate).
 - SC/ST/PH Certificate, from competent authority in case of SC/ST/PH candidate.
 - Transfer/Migration certificate and conduct certificate from the institution last studied.
 - Part time candidates should produce experience certificate and No Objection Certificate.
 - Research Articles (Published in Peer Reviewed Journal)
 - Self attested Photo copy of the all documents (X, H.Sc, UG Mark sheets & Degree, PG Mark sheets & Degree, Community Certificate (OBC/SC/ST/PH) and Transfer Certificate).
 - Scanned copy of all the above listed documents to submitted to <u>shanmuga.dee@pondiuni.ac.in</u>, pueceadmission@gmail.com in Zip format on or before 30.01.2025.
- 3. The candidates who wish to appear for interview should send the following documents to the e-mail id: shanmuga.dee@pondiuni.ac.in, pueceadmission@gmail.com on or before 30/01/2025 (Leave your name and Ph.D. Application registrer number as the subject of the e-mail).
 - Two page research proposal indicating topic of proposed research, objective, methodology and significance.
 - A power point presentation on the topic of proposed research (maximum of 10 slides).
 - The candidates will be allowed for the interview only if they provide the above mentioned documents through email (Research proposal and power point presentation).
- 4. Based on total marks scored by the candidate in both entrance examiniation (70% weightage), and Academic Record, Research Propposal, Presentation, Interview (30% weightage), the list of provisionally selected candidates for dmission, will be displayed in the university website.
- 5. The selected candidates should pay the fees (see the prospectus for the fee details) and take the admission on or before last date mentioned in the final selection list, otherwise the seat will be allotted to the waitlisted candidates.

Important Note:

Venue: Committee Room (SH131), Department of Electronics Engineering, Pondicherry University, Puducherry – 14. Date & Time: 05th February 2025, 09.30 A.M onwards.

- The candidates are expected to come half an hour (9.00AM) before the said time.
- You are called for the interview on the information given by you in the application form and subject to you fulfilling the minimum eligibility requirements and other conditions as laid down by the University in the prospectus 2024-2025.
- If it is found, at any stage, that the information provided by you is false or incorrect or you also do not fulfill the eligibility criteria you will not be considered for admission.
- If required you must be prepared to stay for one or two days till interviews are completed.
- Candidate should bring the PPT in USB drive, system will be arranged in the department.

No mobile communication will be made to the candidates. It is the responsibility of the candidate to check their registered email ids everyday for email from shanmuga.dee@pondiuni.ac.in , pueceadmission@gmail.com and also university website for further updates.

For any queries contact:

e-mail: shanmuga.dee@pondiuni.ac.in, pueceadmission@gmail.com/Phone: 0413 - 2654992/997

20/01/2025 Head of the Department Dr. T. SHANMUGANANTHAM Professor & Head Department of Electronics Engineering

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